

SPECIFICATION

MODEL: 012-LAP-SSI Interface-M

PART NO: _____

VERSION: V1.09

Approver		Check	Design
GM	PM		

Customer Confirm

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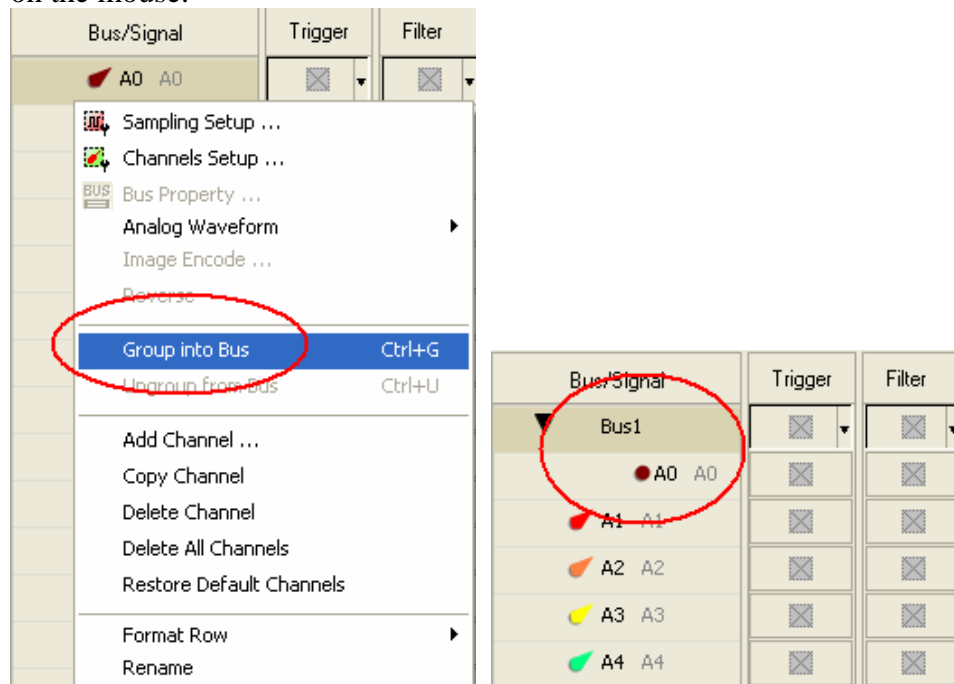
1 Software Register

Please register the software as the following steps:

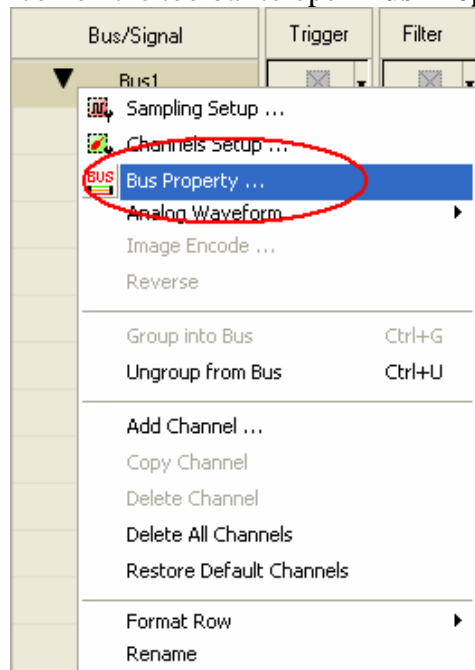
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

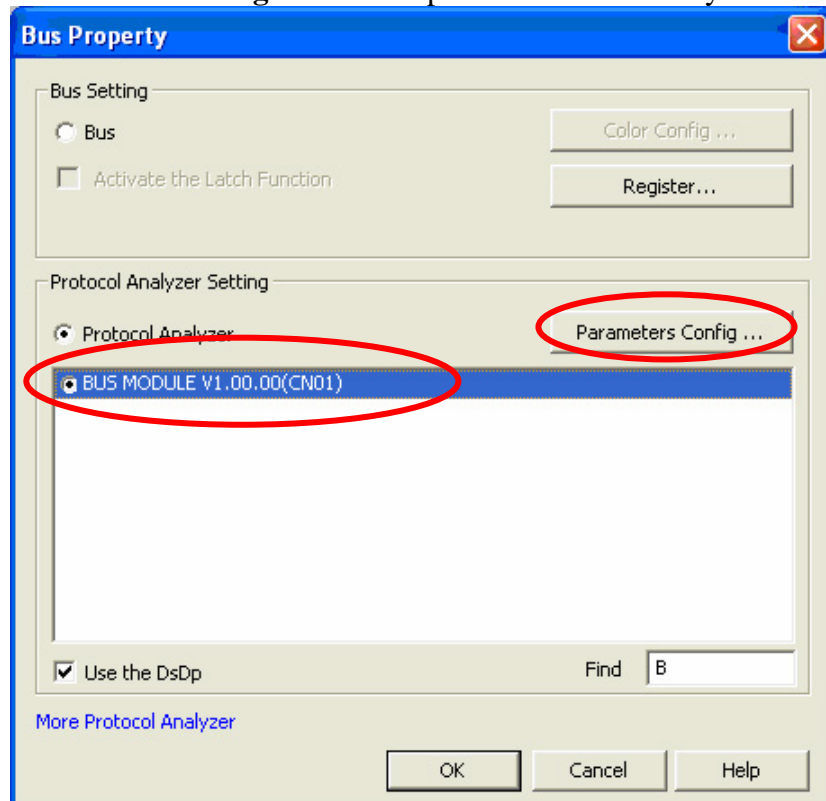
STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key** on the mouse.



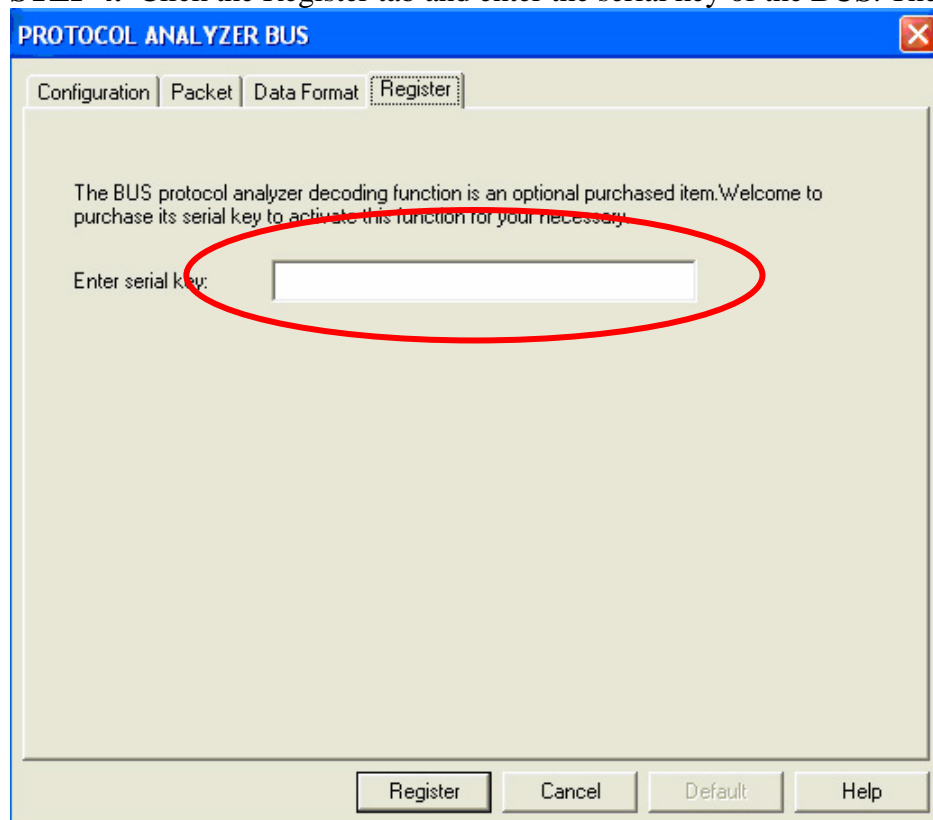
STEP 2. Select **Bus1**, and press **Right Key** on the mouse to list the menu, then click **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.



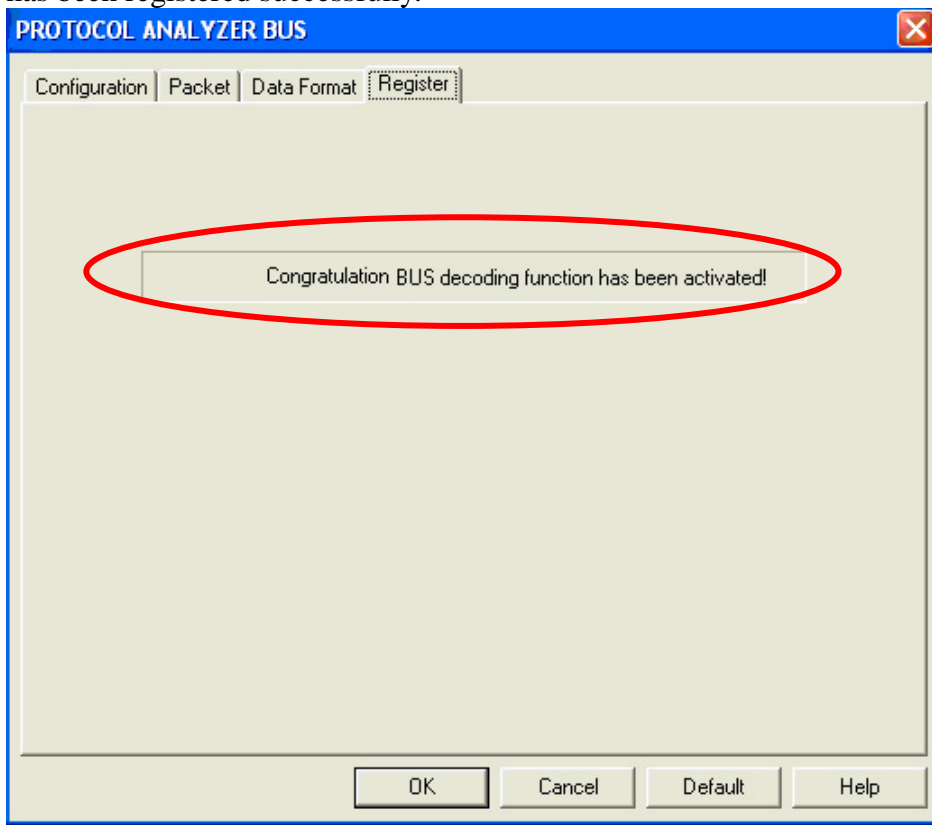
STEP 3. Select the Protocol Analyzer, and then choose **BUS MODULE V1.00.00 (CN01)**. Next click **Parameters Configuration** to open the Protocol Analyzer Bus dialog box.



STEP 4. Click the Register tab and enter the serial key of the **BUS**. Then click **Register**.



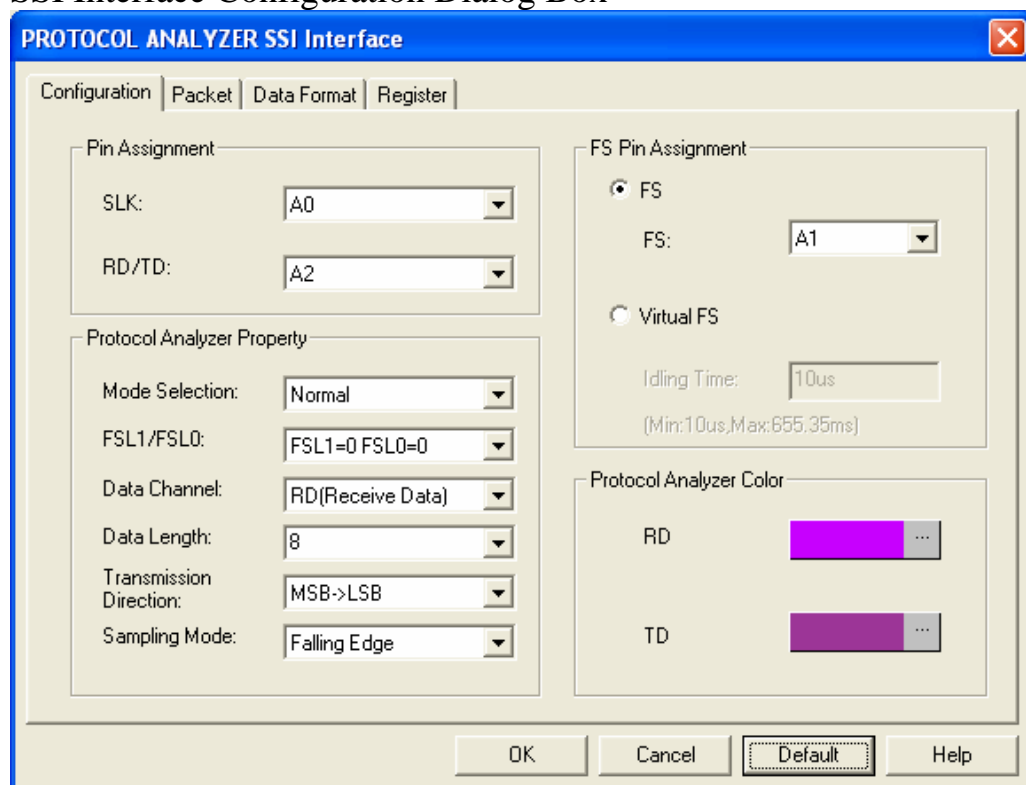
STEP 5. After clicking the Register button, the following dialog box will appear; it denotes that the BUS has been registered successfully.



2 User Interface

In the configuration, please refer to below images to select options of setting **SSI Interface** Module.

SSI Interface Configuration Dialog Box



Pin Assignment:

SLK: It is the Clock channel, the default is A0.

RD/TD: It is the Data channel, the default is A2.

Protocol Analyzer Property:

Mode Selection: Set the Mode to Normal or Network.

FSL1/FSL0: Set the FSL1/FSL0 to the option (FSL1=0 FSL0=0, FSL1=1 FSL0=0, FSL1=0 FSL0=1 or FSL1=1 FSL0=1) from the pull-down menu.

Data Channel: Set the Channel to RD (Receive Data) or TD (Trans Data).

Data Length: Set the Length to the option (8, 12, 16 or 24) or set the Length in the range from 4 to 32.

Transmission Direction: Set the Direction to MSB->LSB or LSB->MSB, the default is MSB->LSB.

Sampling Mode: Set the Mode to Rising Edge or Falling Edge.

FS Pin Assignment:

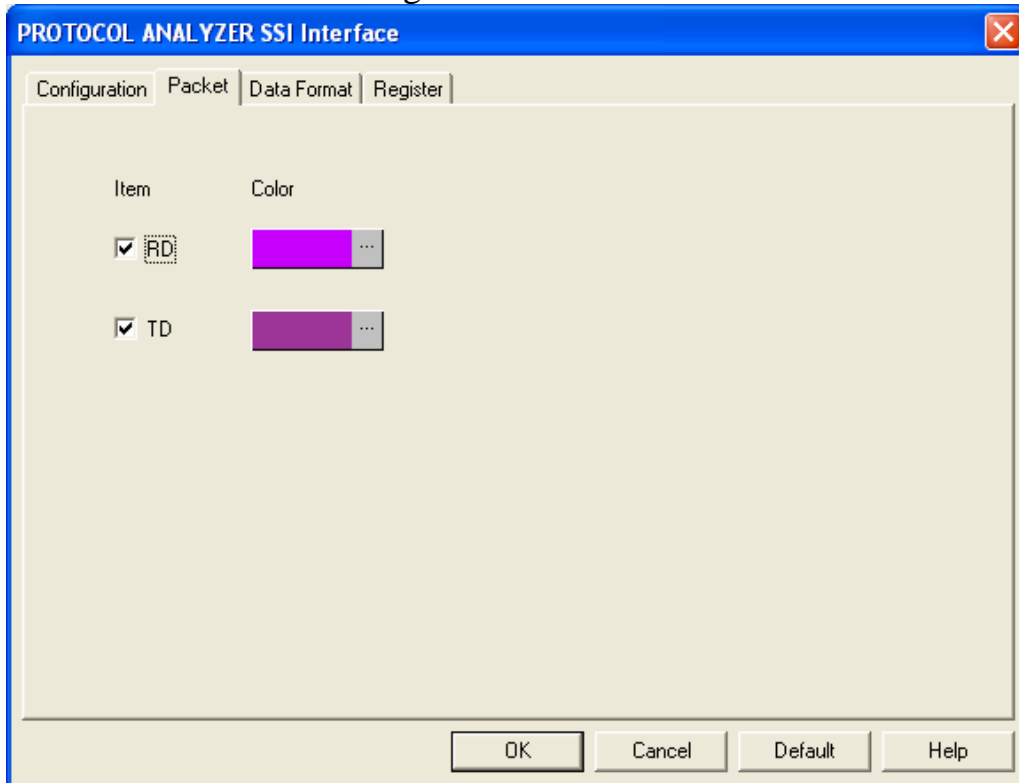
FS: The default is A1.

Virtual FS: Set the Idling Time to 20ns when the Sampling Frequency is 50MHz.

Protocol Analyzer color:

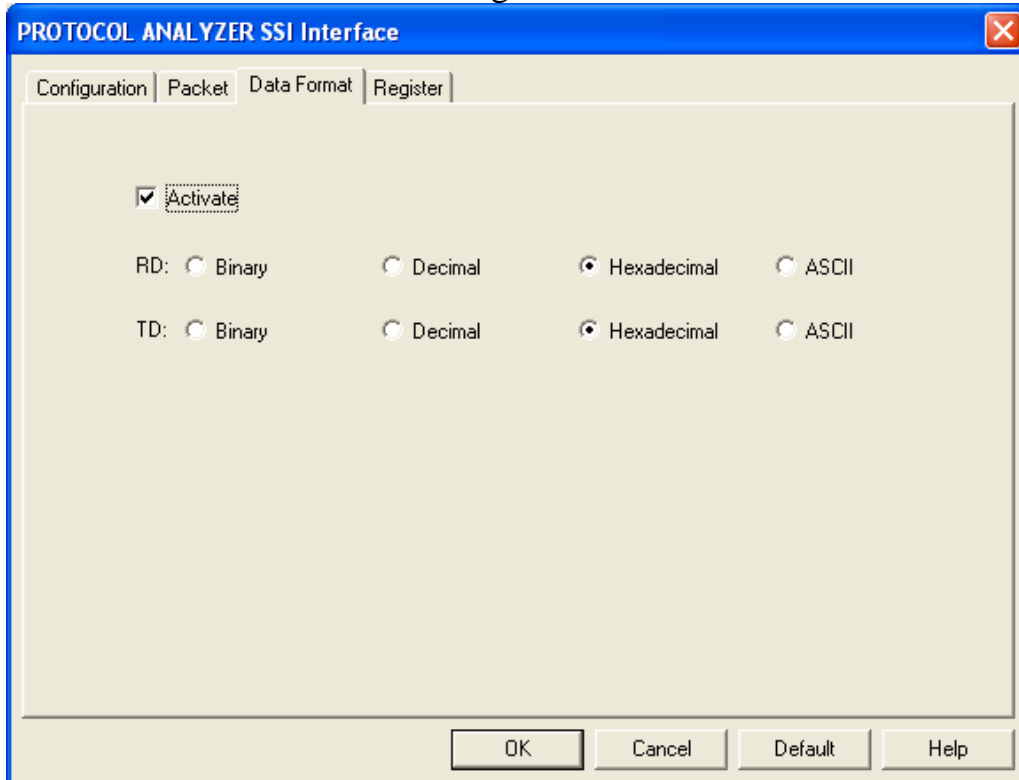
The protocol analyzer color can be varied by users.

SSI Interface Packet Dialog Box



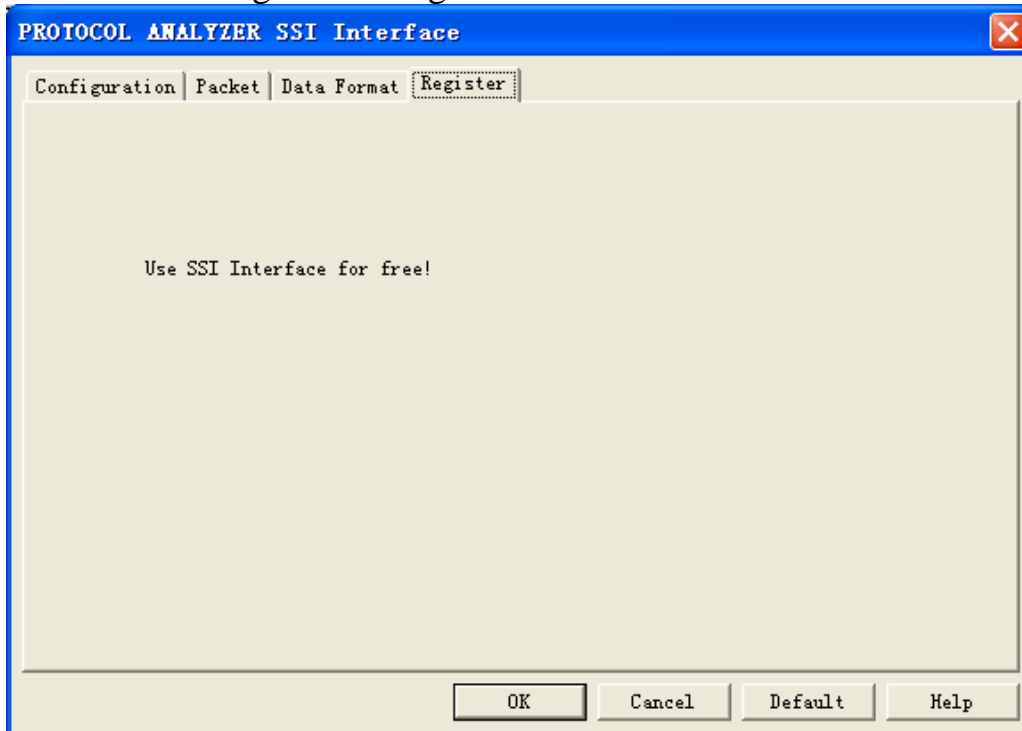
In the Packet dialog box, users can set the items to be displayed and the color of items.

SSI Interface Data Format Dialog Box



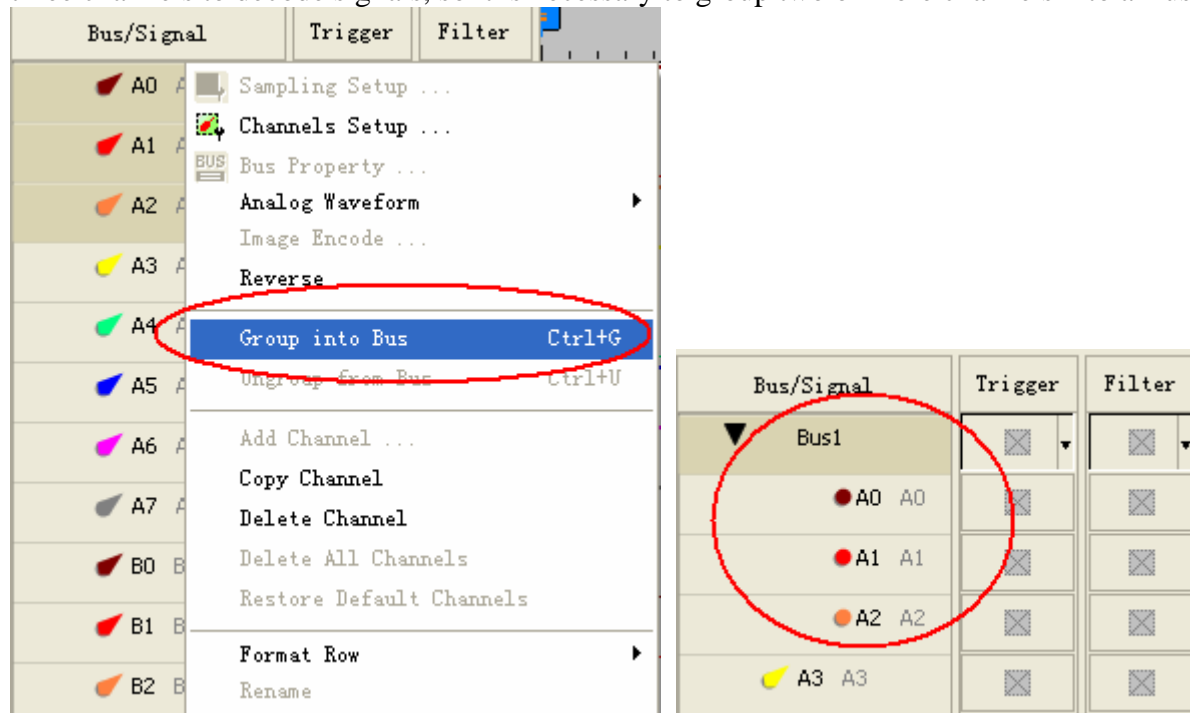
Users can set the Data Format of the RD and TD as their requirements. When selecting the option, Activate, the data formats are decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data formats are decided by the settings in the main program.

SSI Interface Register Dialog Box

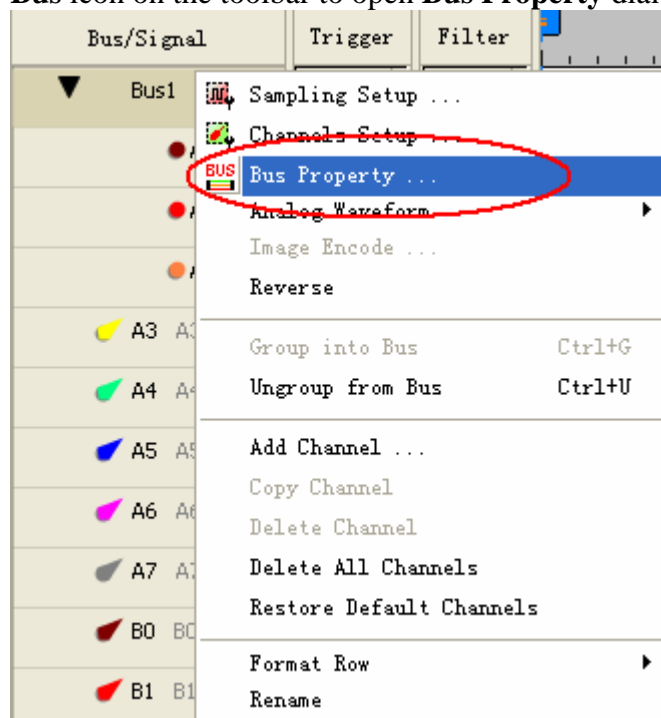


3 Operating Instructions

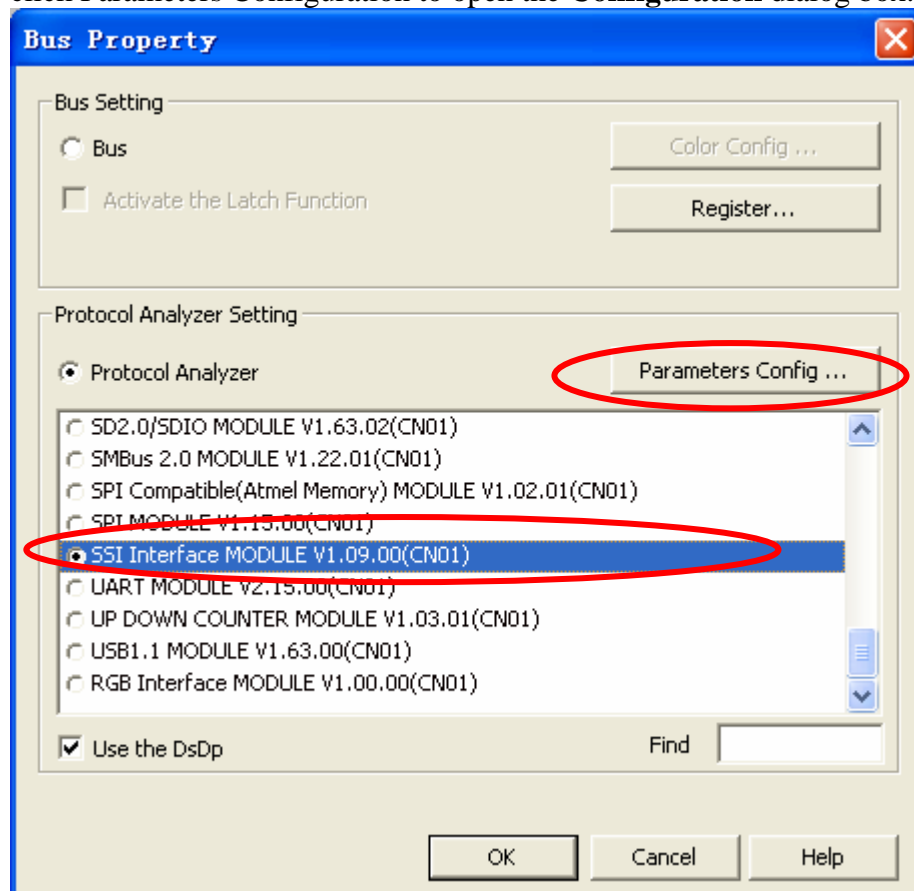
STEP 1. Group A0-A2 into **Bus1** by pressing the **Right Key** on the mouse. SSI Interface needs two or three channels to decode signals, so it is necessary to group two or more channels into a Bus.



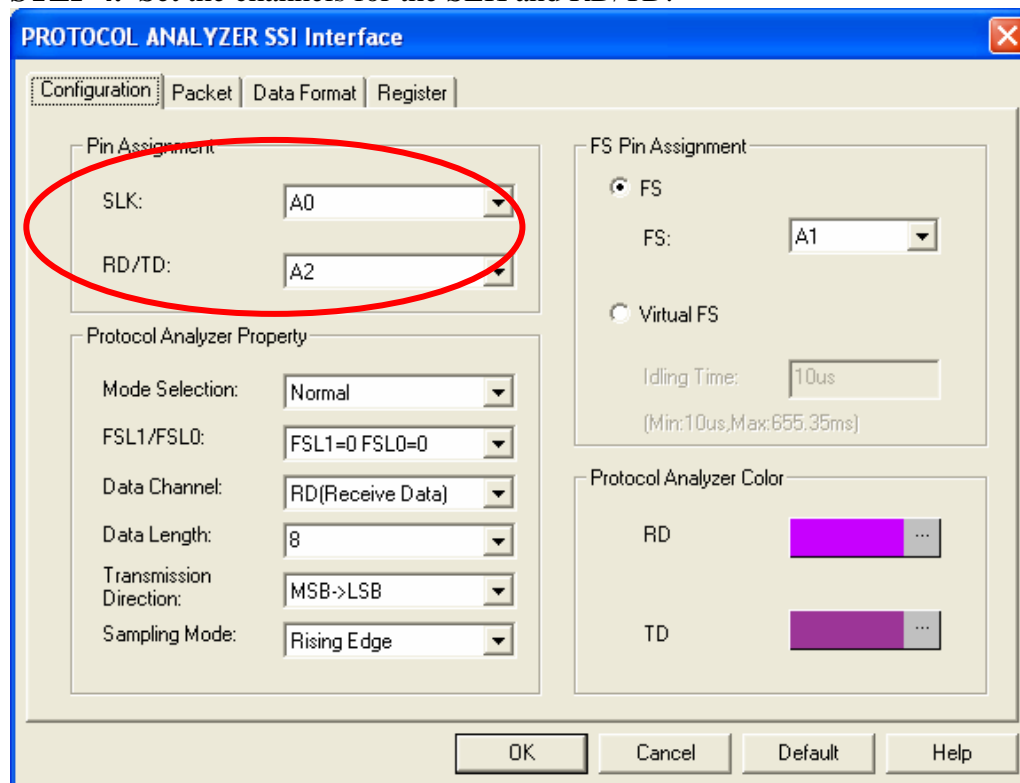
STEP 2. Selected **Bus 1**, and press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.



STEP 3. Select Protocol Analyzer, and then choose **SSI Interface MODULE V1.09.00(CN01)**. Next click Parameters Configuration to open the **Configuration** dialog box.



STEP 4. Set the channels for the SLK and RD/TD.



STEP 5. Set the **Mode Selection** to Normal or Network.

PROTOCOL ANALYZER SSI Interface

Configuration | Packet | Data Format | Register

Pin Assignment

SLK: A0

RD/TD: A2

Protocol Analyzer Property

Mode Selection: Normal

FSL1/FSL0: FSL1=0 FSL0=0

Data Channel: RD(Receive Data)

Data Length: 8

Transmission Direction: MSB->LSB

Sampling Mode: Rising Edge

FS Pin Assignment

FS

FS: A1

Virtual FS

Idling Time: 10us

(Min:10us,Max:655.35ms)

Protocol Analyzer Color

RD

TD

OK Cancel Default Help

STEP 6. Set the **FSL1/FSL0** to the option (FSL1=0 FSL0=0, FSL1=1 FSL0=0, FSL1=0 FSL0=1 or FSL1=1 FSL0=1) from the pull-down menu.

PROTOCOL ANALYZER SSI Interface

Configuration | Packet | Data Format | Register

Pin Assignment

SLK: A0

RD/TD: A2

Protocol Analyzer Property

Mode Selection: Normal

FSL1/FSL0: FSL1=0 FSL0=0

Data Channel: RD(Receive Data)

Data Length: 8

Transmission Direction: MSB->LSB

Sampling Mode: Rising Edge

FS Pin Assignment

FS

FS: A1

Virtual FS

Idling Time: 10us

(Min:10us,Max:655.35ms)

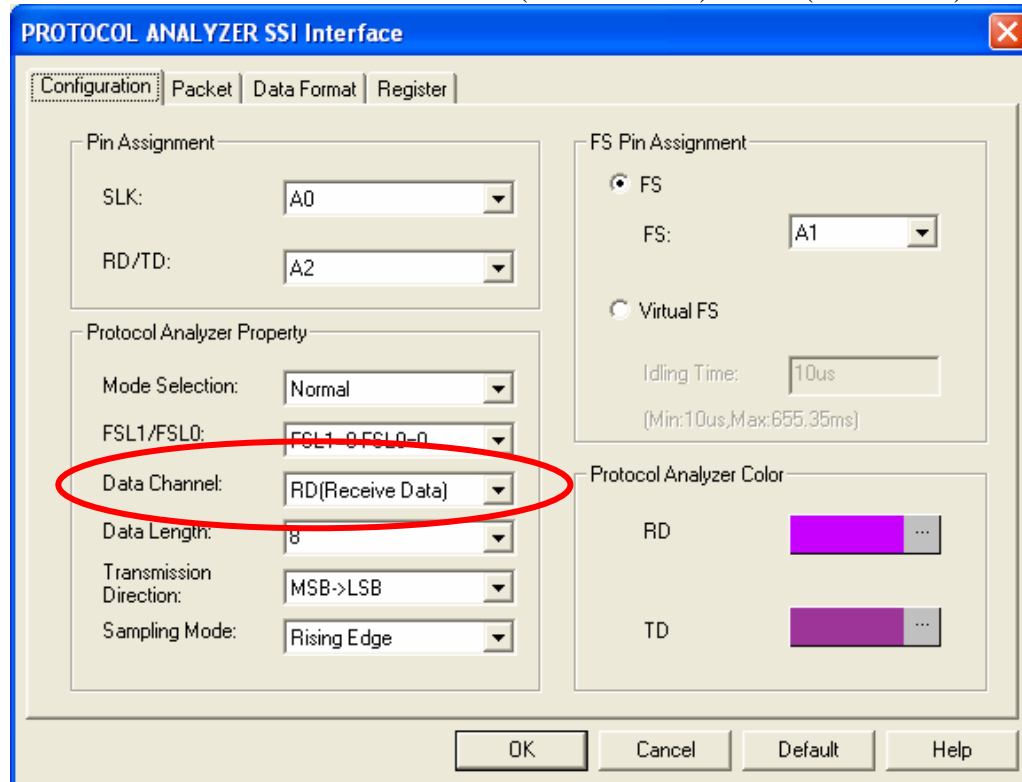
Protocol Analyzer Color

RD

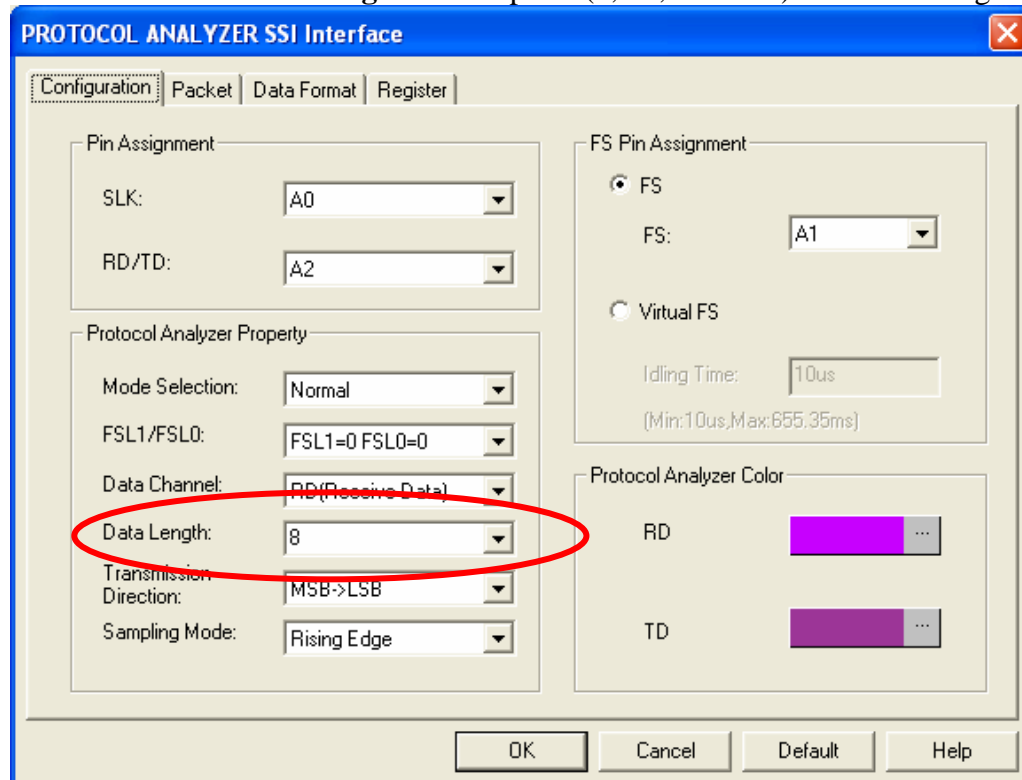
TD

OK Cancel Default Help

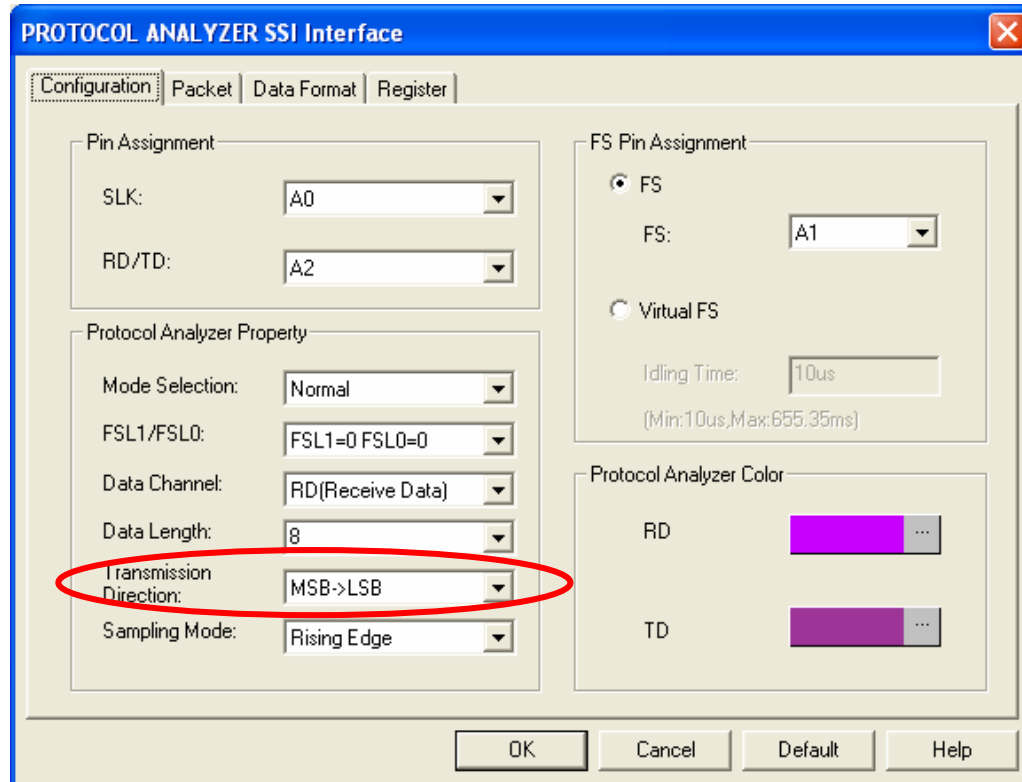
STEP 7. Set the **Data Channel** to RD (Receive Data) or TD (Trans Data).



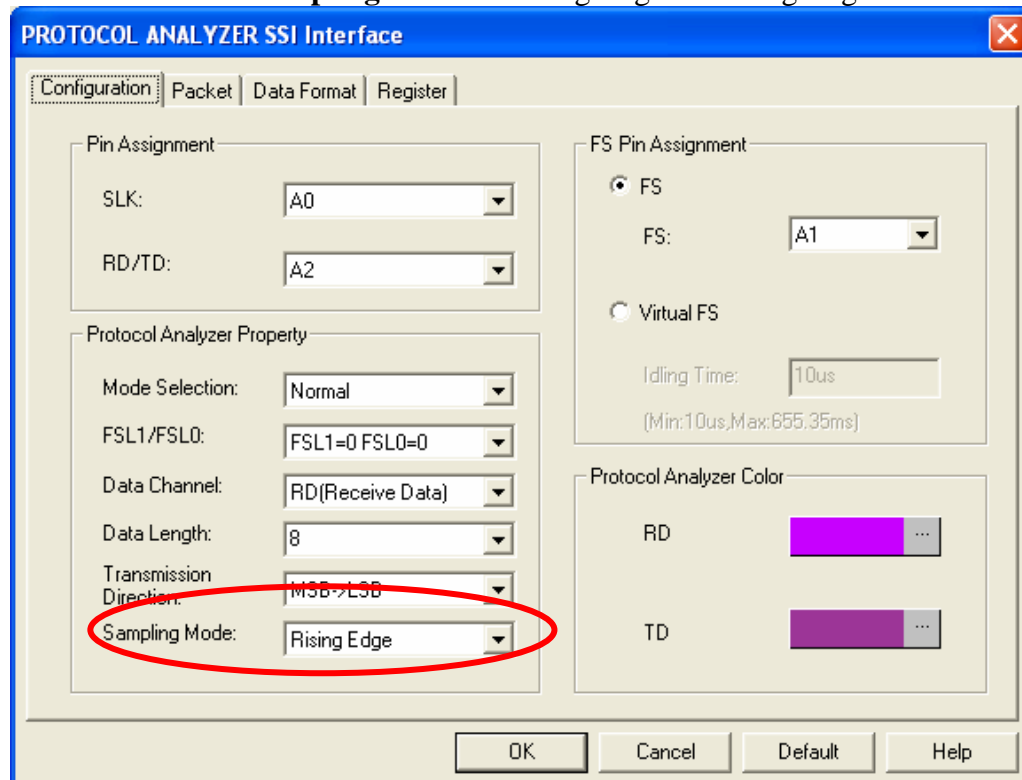
STEP 8. Set the **Data Length** to the option (8, 12, 16 or 24) or set the Length in the range from 4 to 32.



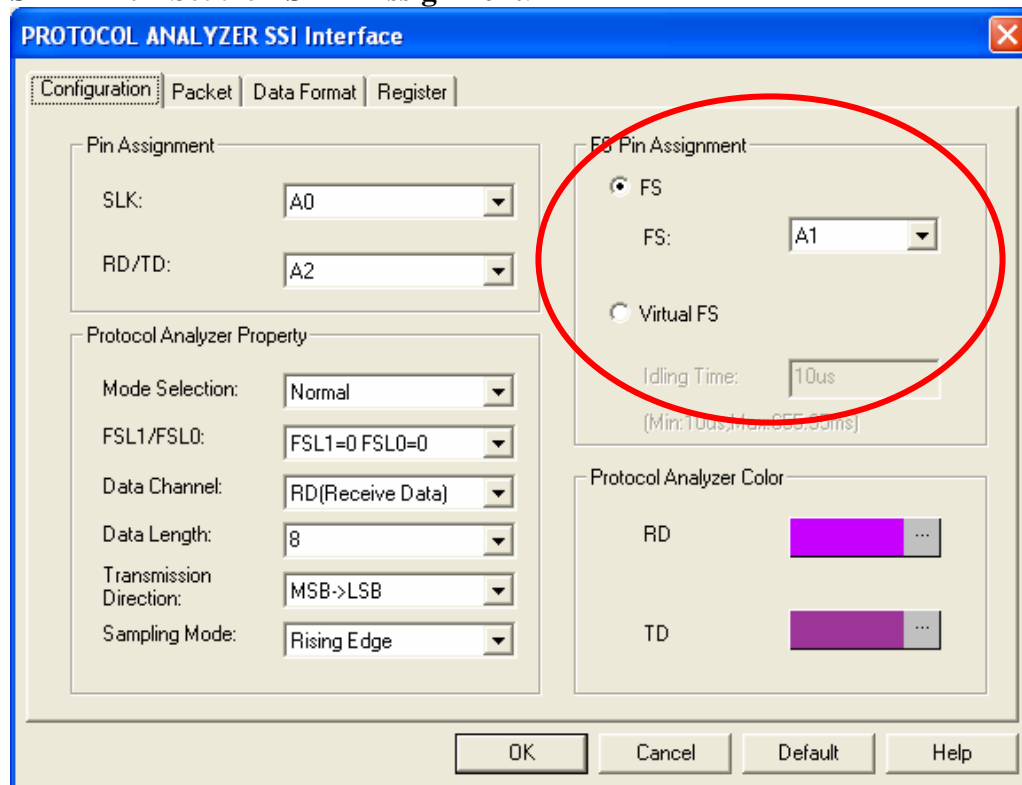
STEP 9. Set the **Transmission Direction** to MSB->LSB or LSB->MSB.



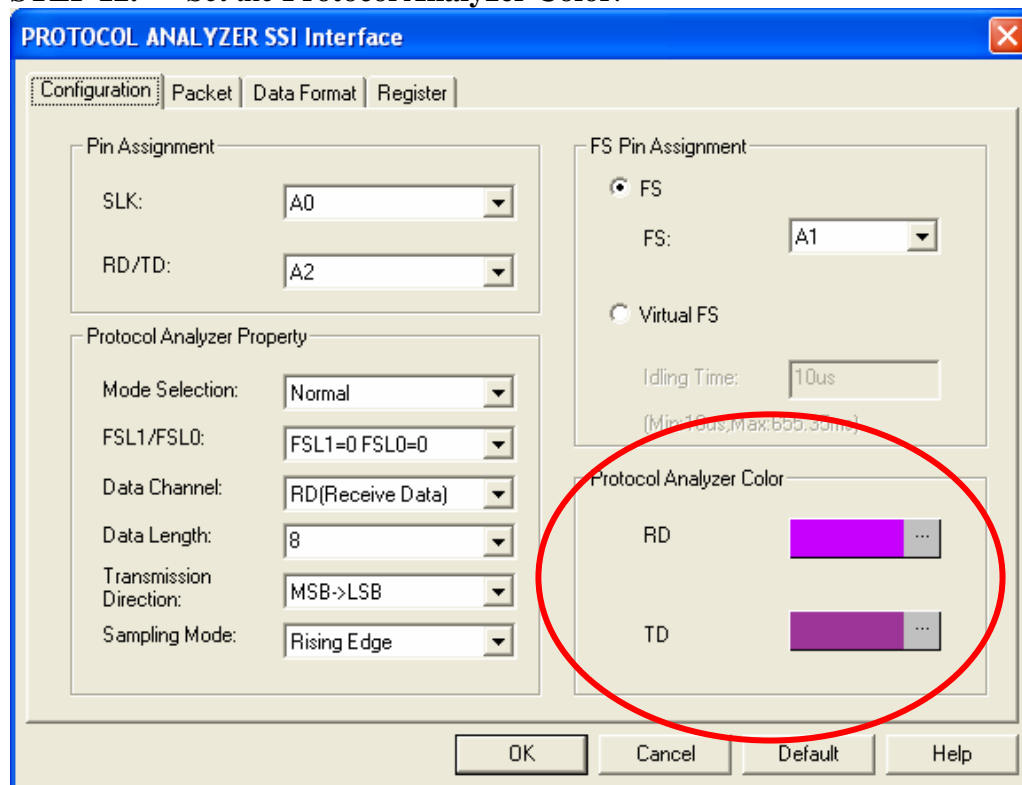
STEP 10. Set the **Sampling Mode** to Rising Edge or Falling Edge



STEP 11. Set the FS Pin Assignment.

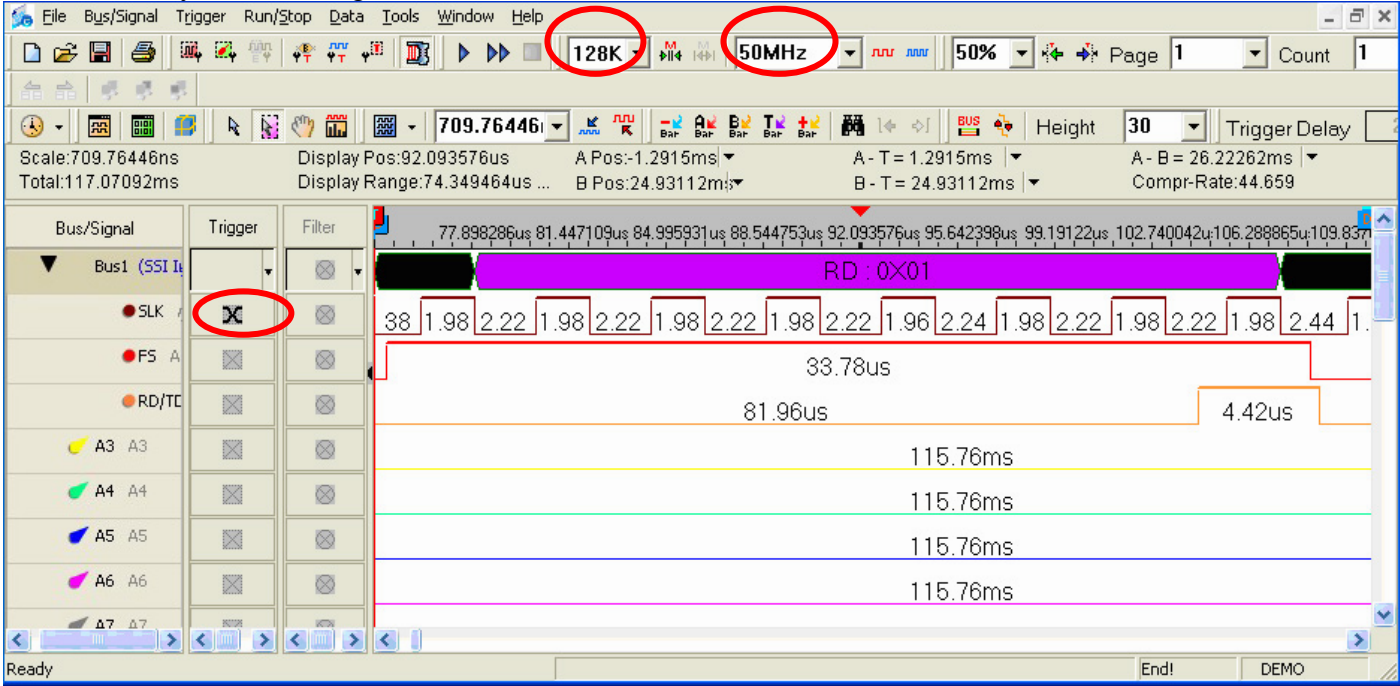


STEP 12. Set the Protocol Analyzer Color.



STEP 13. Following pictures show the completion of the protocol analyzer decoding and packet list. The trigger condition is set as Either Edge; the memory depth is 128K; the sampling frequency is 50MHz (the sampling frequency should be more than 4 times higher than the signal to be tested).

Protocol Analyzer Decoding



Packet List

